



ASMEX.320A

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant	:	Pomarede et al.)	Group Art Unit 2812
)	
App. No.	:	10/074,722)	
)	
Filed	:	February 11, 2002)	
)	
For	:	INTEGRATION OF HIGH K)	
		GATE DIELECTRIC)	
)	
Examiner	:	Pompey, Ron Everett)	

INFORMATION DISCLOSURE STATEMENT

United States Patent and Trademark Office
P.O. Box 2327
Arlington, VA 22202

Dear Sir:

Enclosed is form PTO-1449 listing references that are also enclosed. This Information Disclosure Statement is being filed before the mailing date of a final action under 37 C.F.R. § 1.113 and before the mailing date of a Notice of Allowance under § 1.311. A certification under 37 C.F.R. § 1.97(e) is set forth below. Thus, no fee is required as set forth in 37 C.F.R. § 1.97(c).

CERTIFICATION UNDER 37 C.F.R. § 1.97(e)(2)

I hereby certify that no item of information contained in this Statement was cited in a communication from a foreign Patent Office in a counterpart foreign application, or, to my knowledge after making reasonable inquiry, was known to any individual designated in 37 C.F.R.

Appl. No. : 10/074,722
Filed : February 11, 2002

§ 1.56(c) more than three months prior to the filing of this Information Disclosure Statement.

Respectfully submitted,

KNOBBE, MARTENS, OLSON & BEAR, LLP

Dated: 4/8/03

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